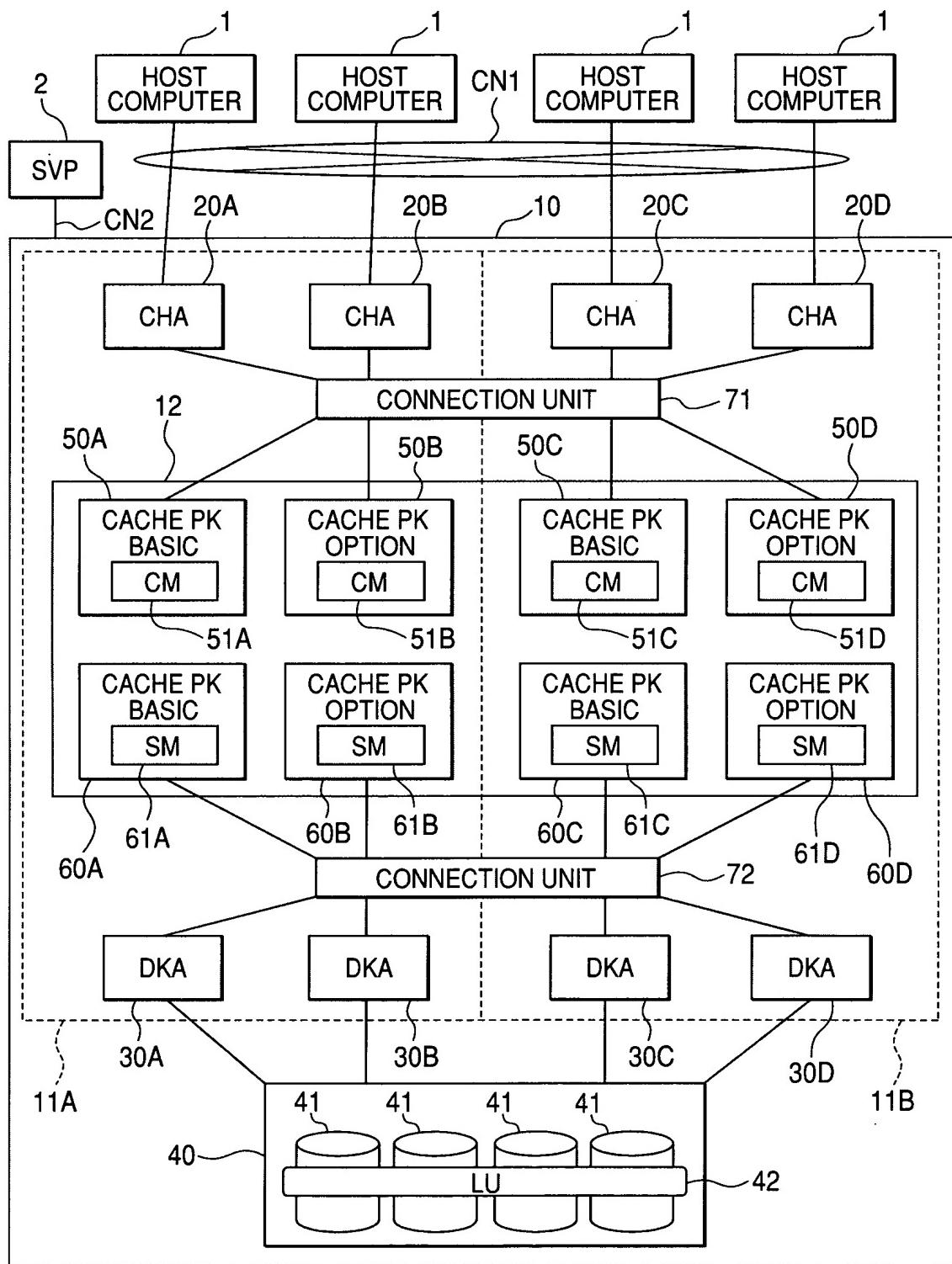
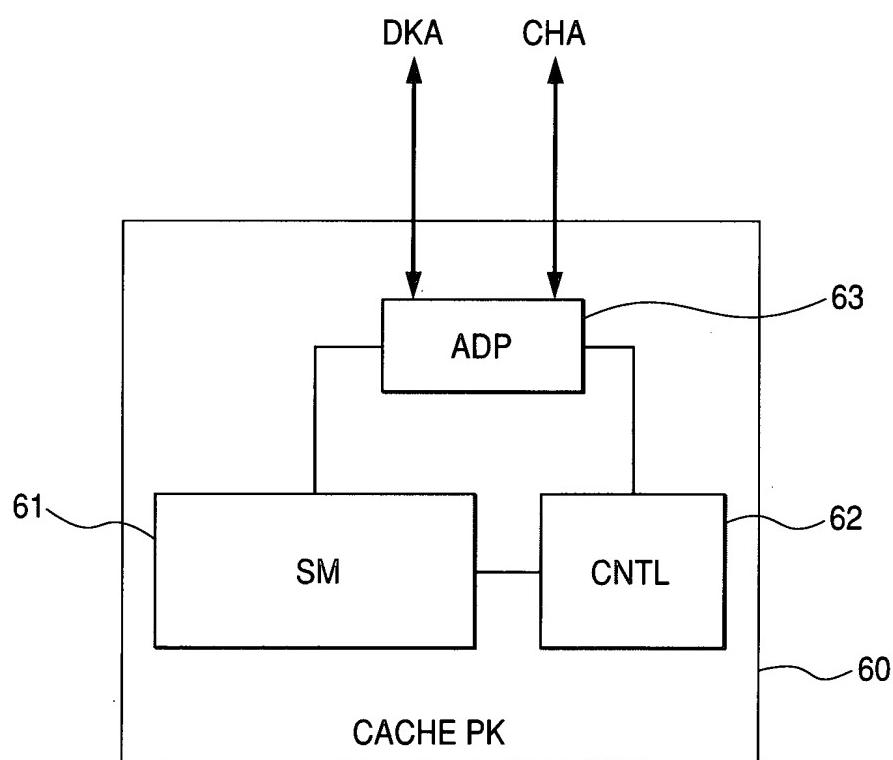


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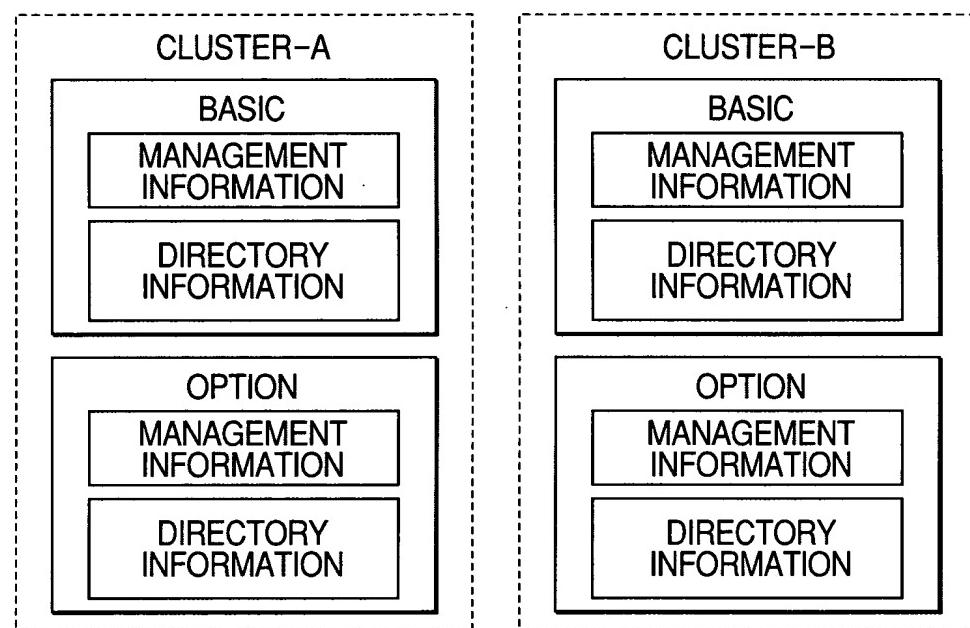
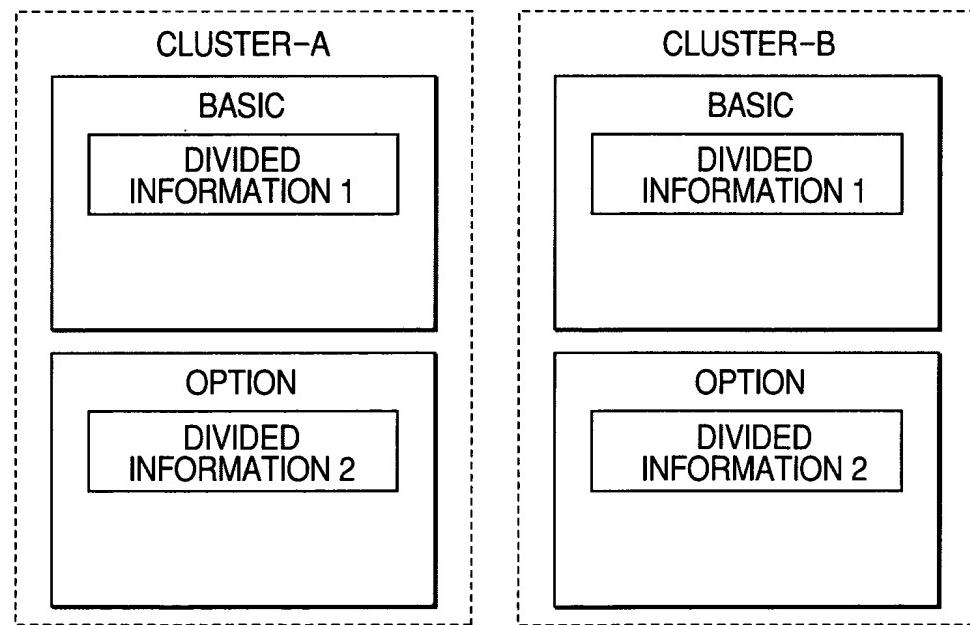
FIG. 1



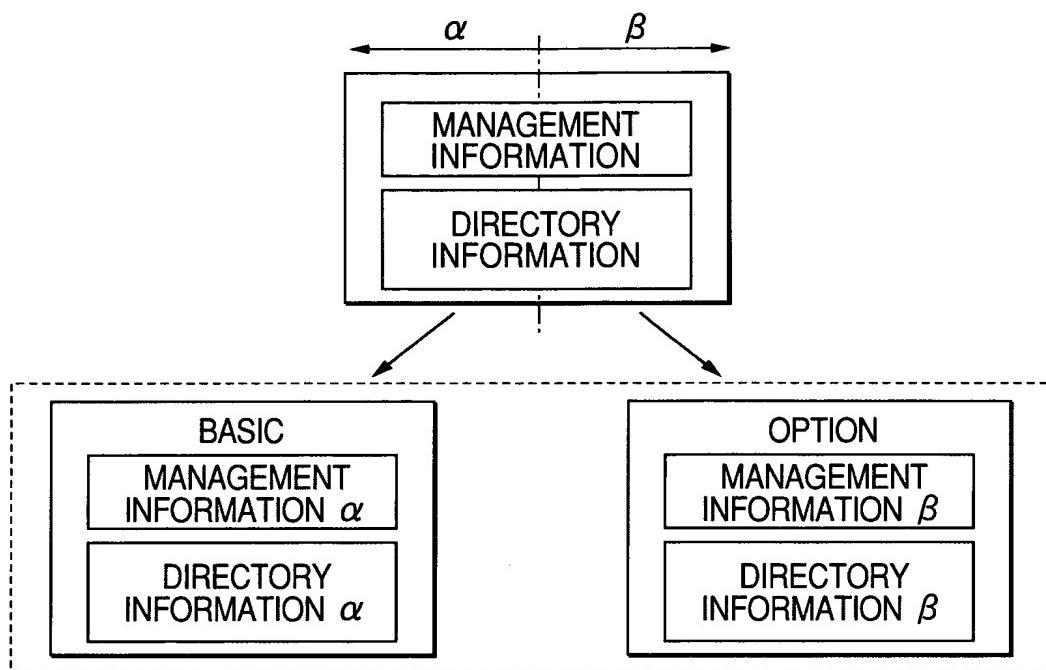
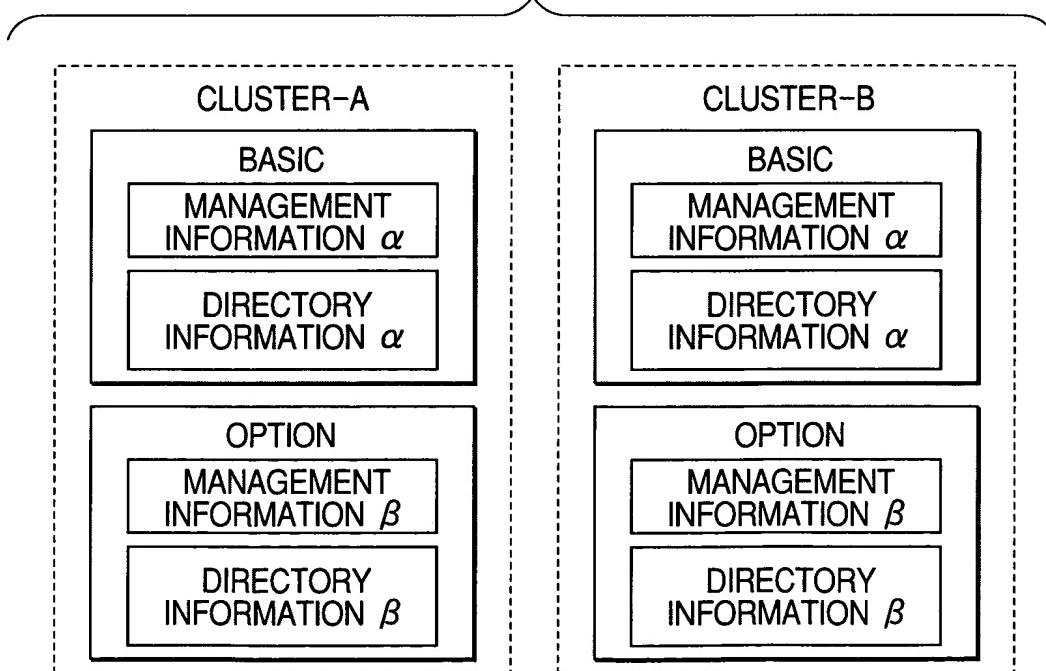
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FIG. 2

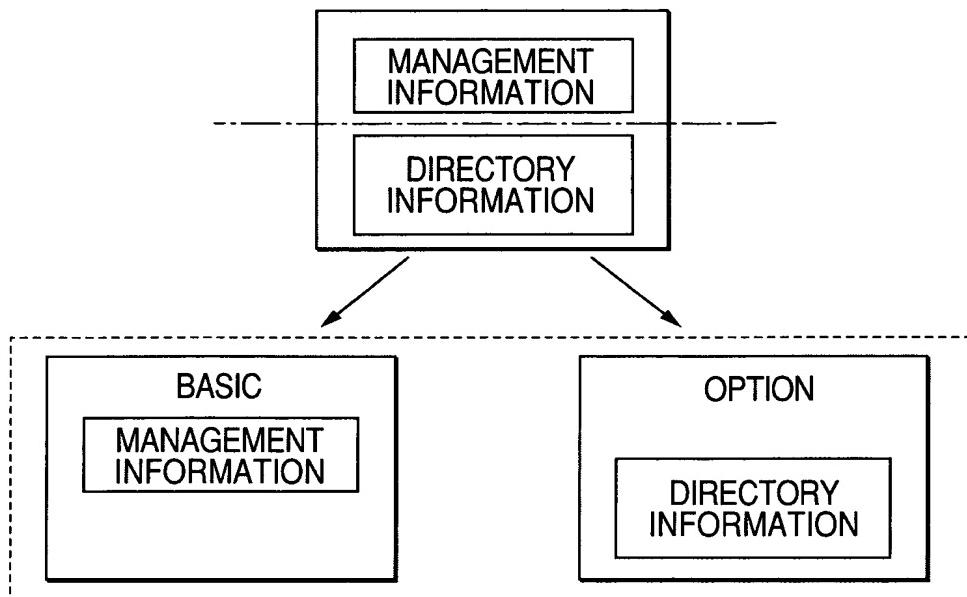
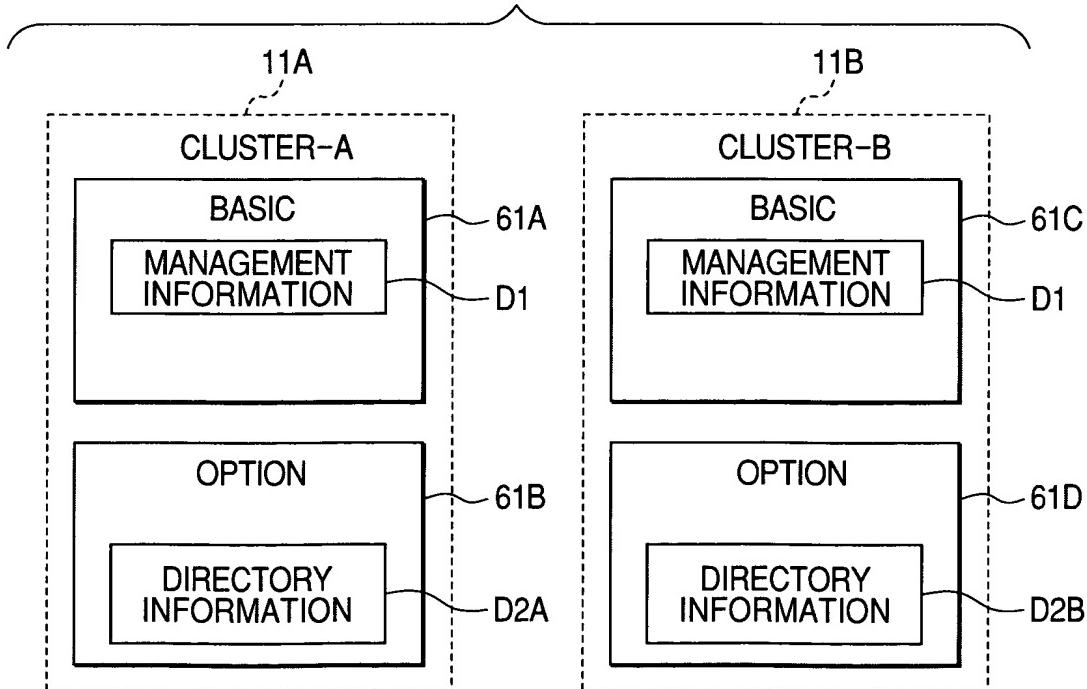
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FIG. 3A**FIG. 3B**

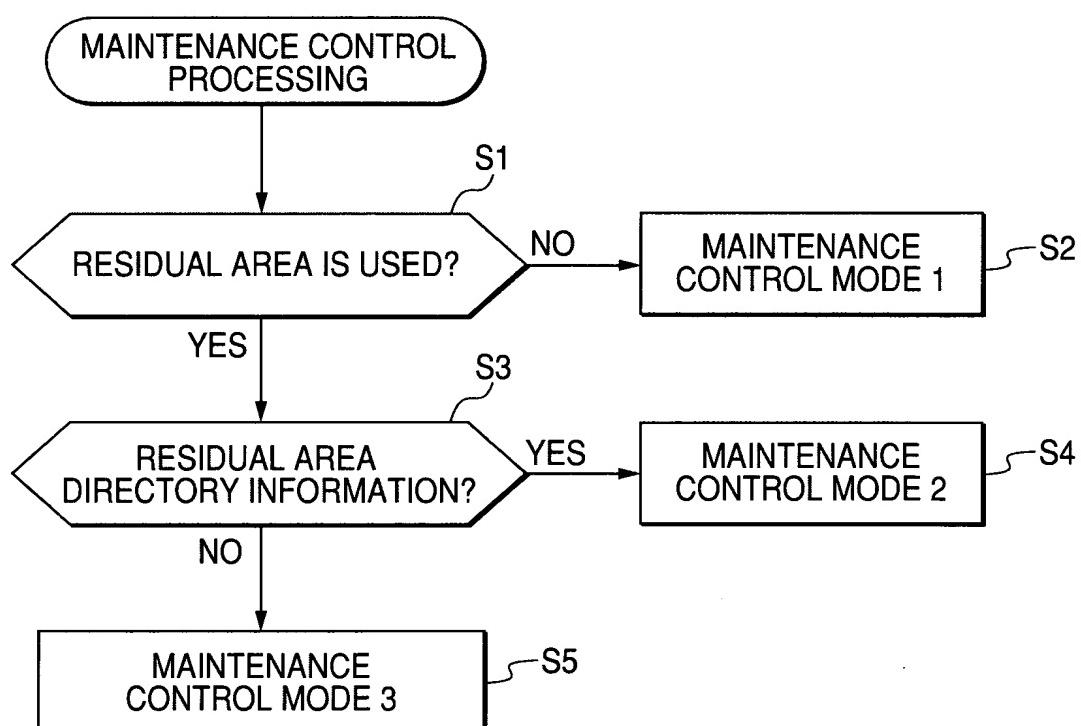
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FIG. 4A**FIG. 4B**

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FIG. 5A**FIG. 5B**

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FIG. 6

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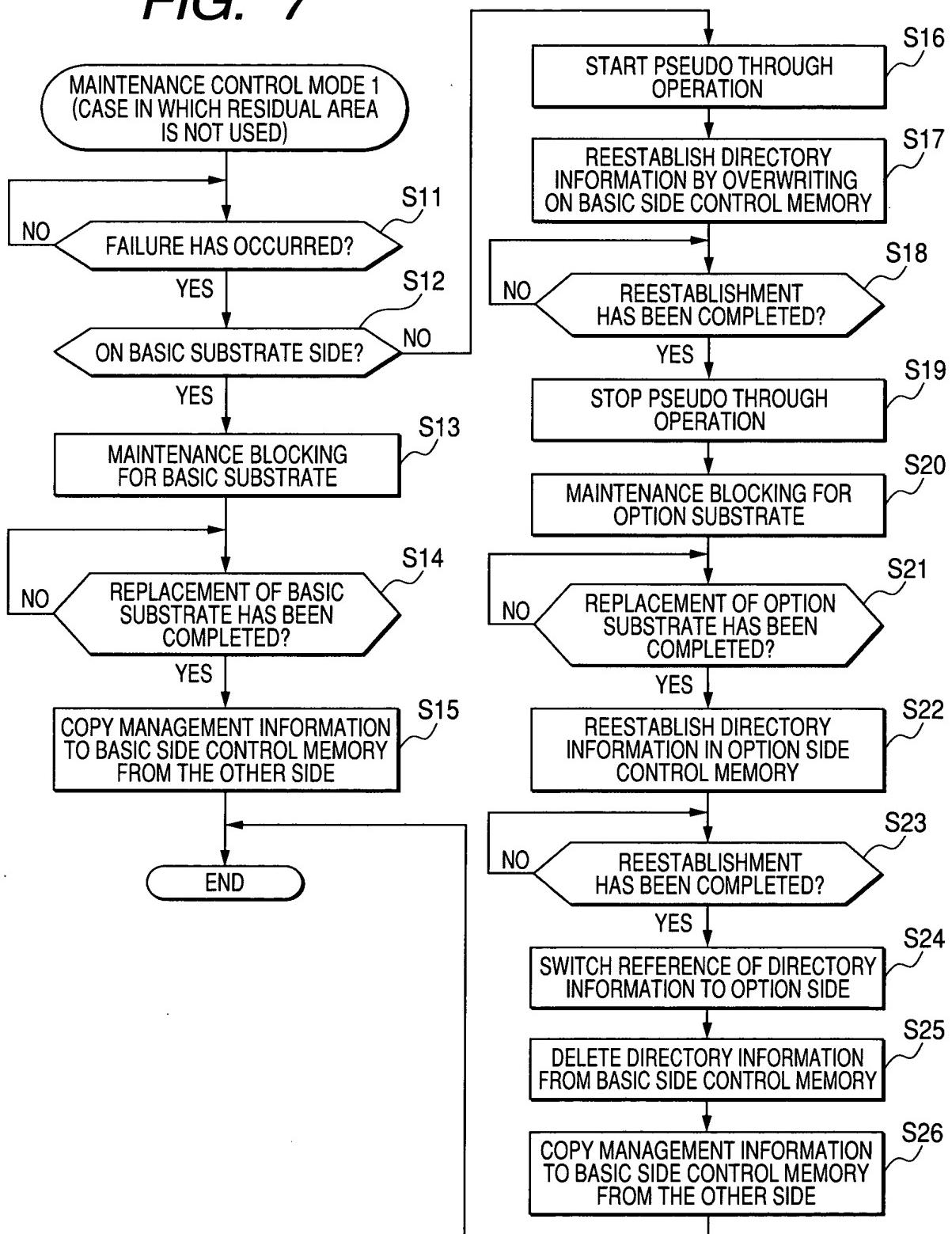
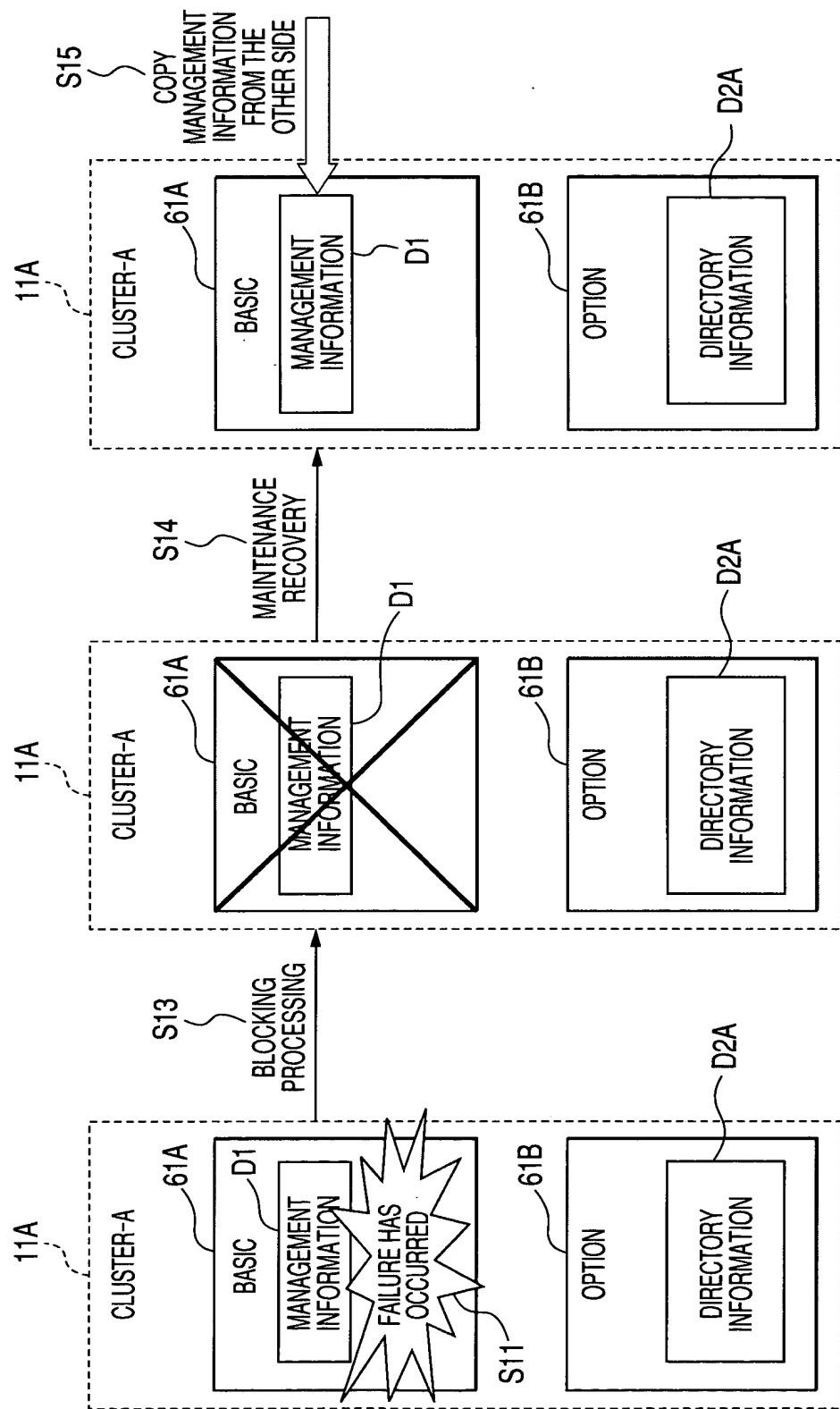
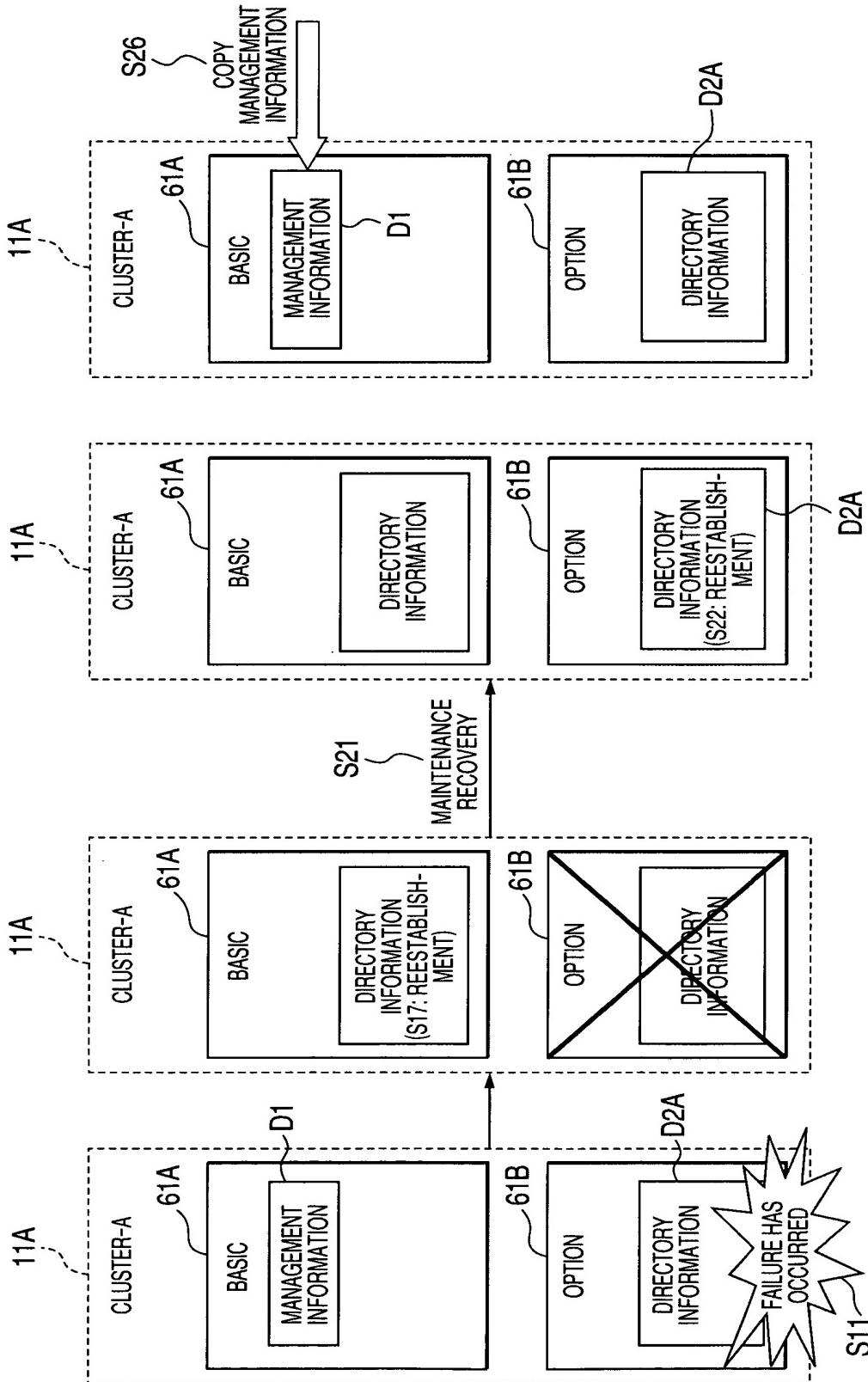
FIG. 7

FIG. 8A
FIG. 8B
FIG. 8C

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FIG. 9A FIG. 9B FIG. 9C FIG. 9D



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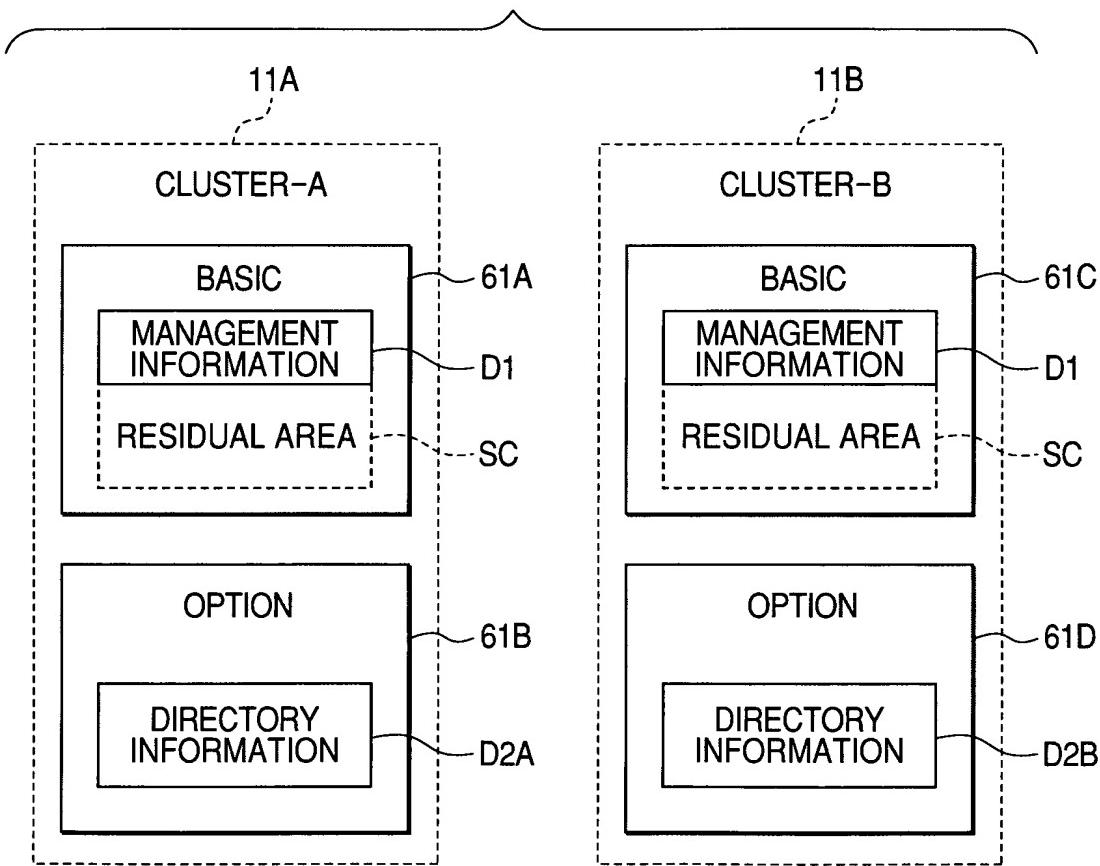
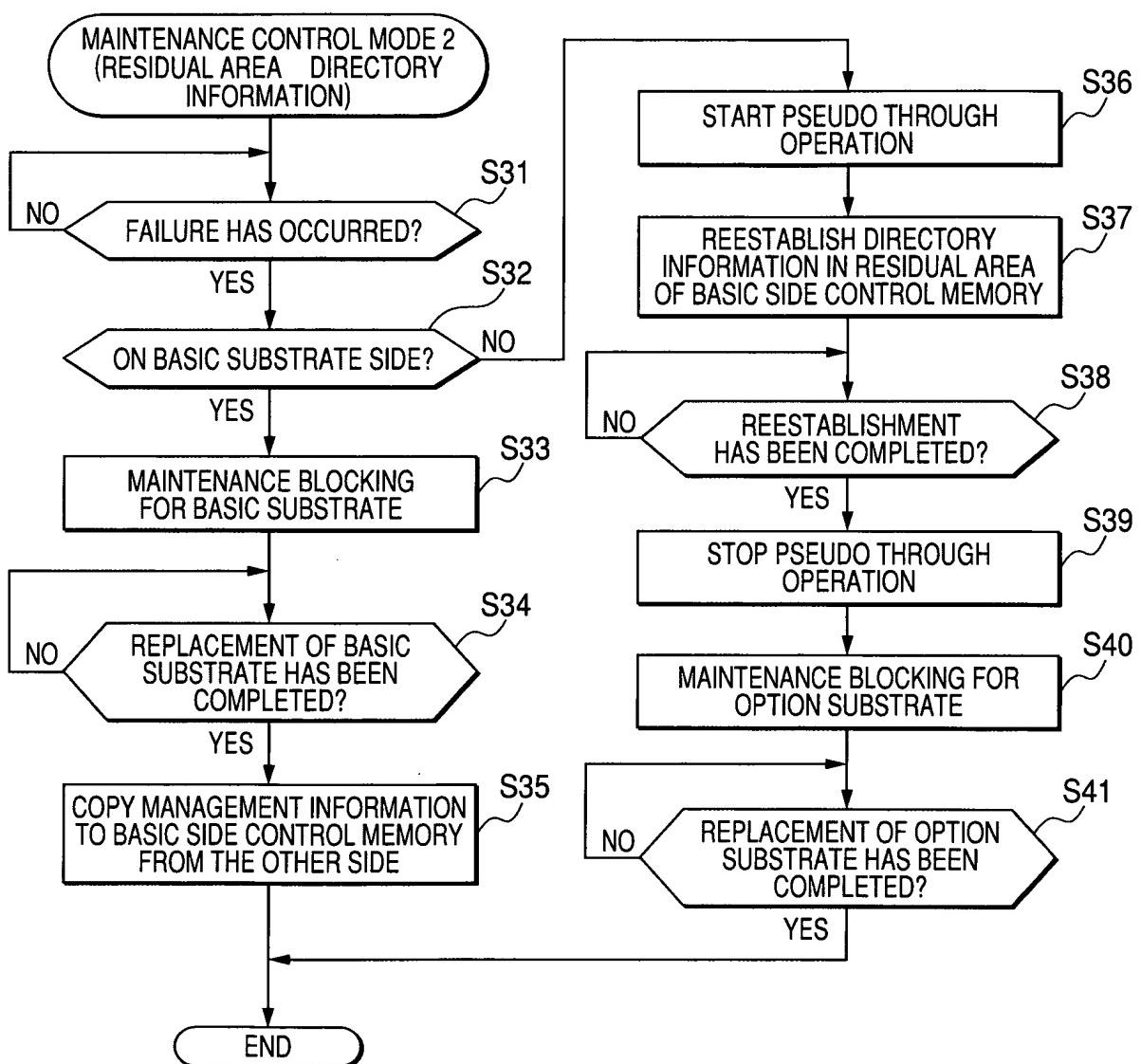
FIG. 10

FIG. 11



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FIG. 12A

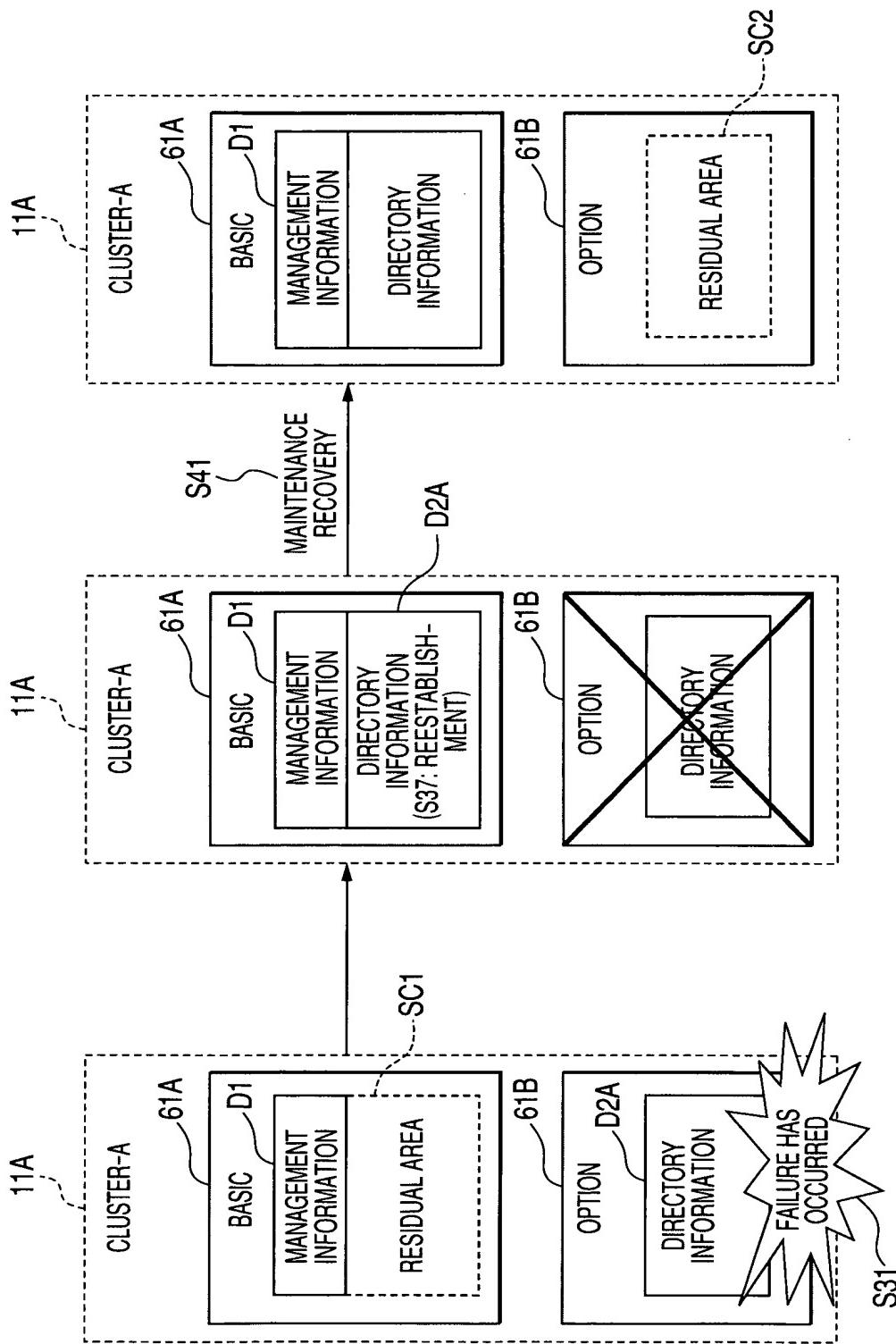


FIG. 12B

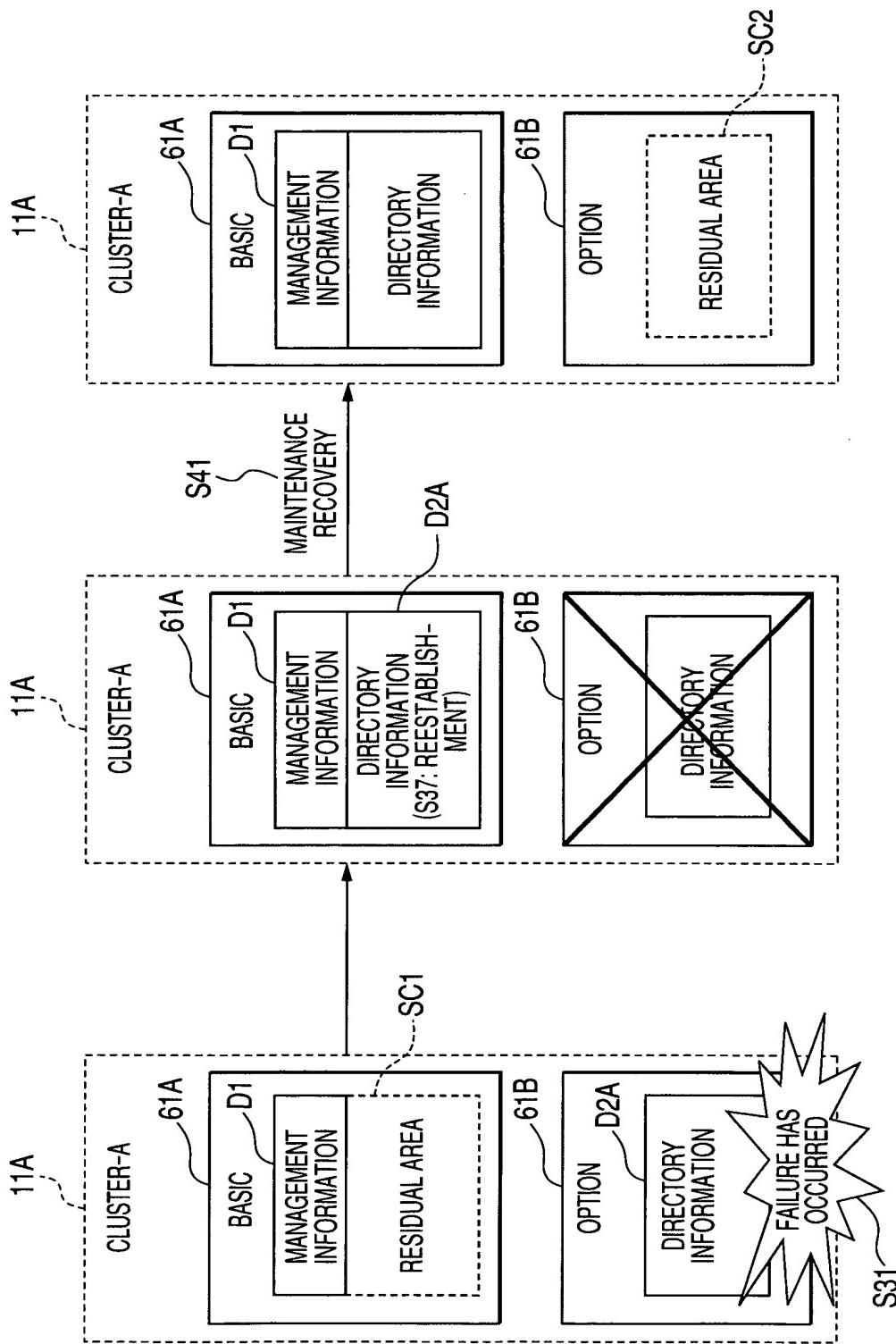
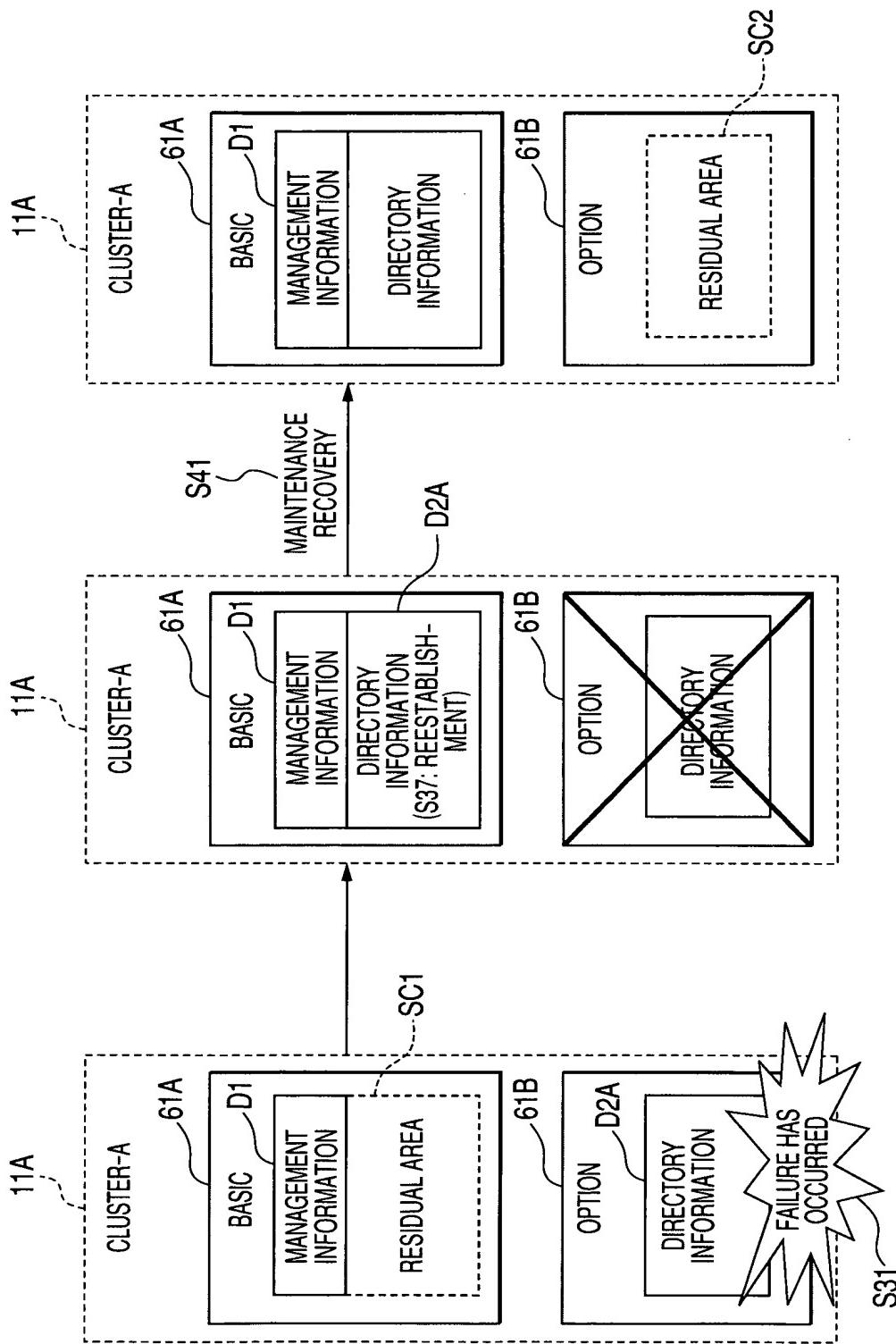


FIG. 12C



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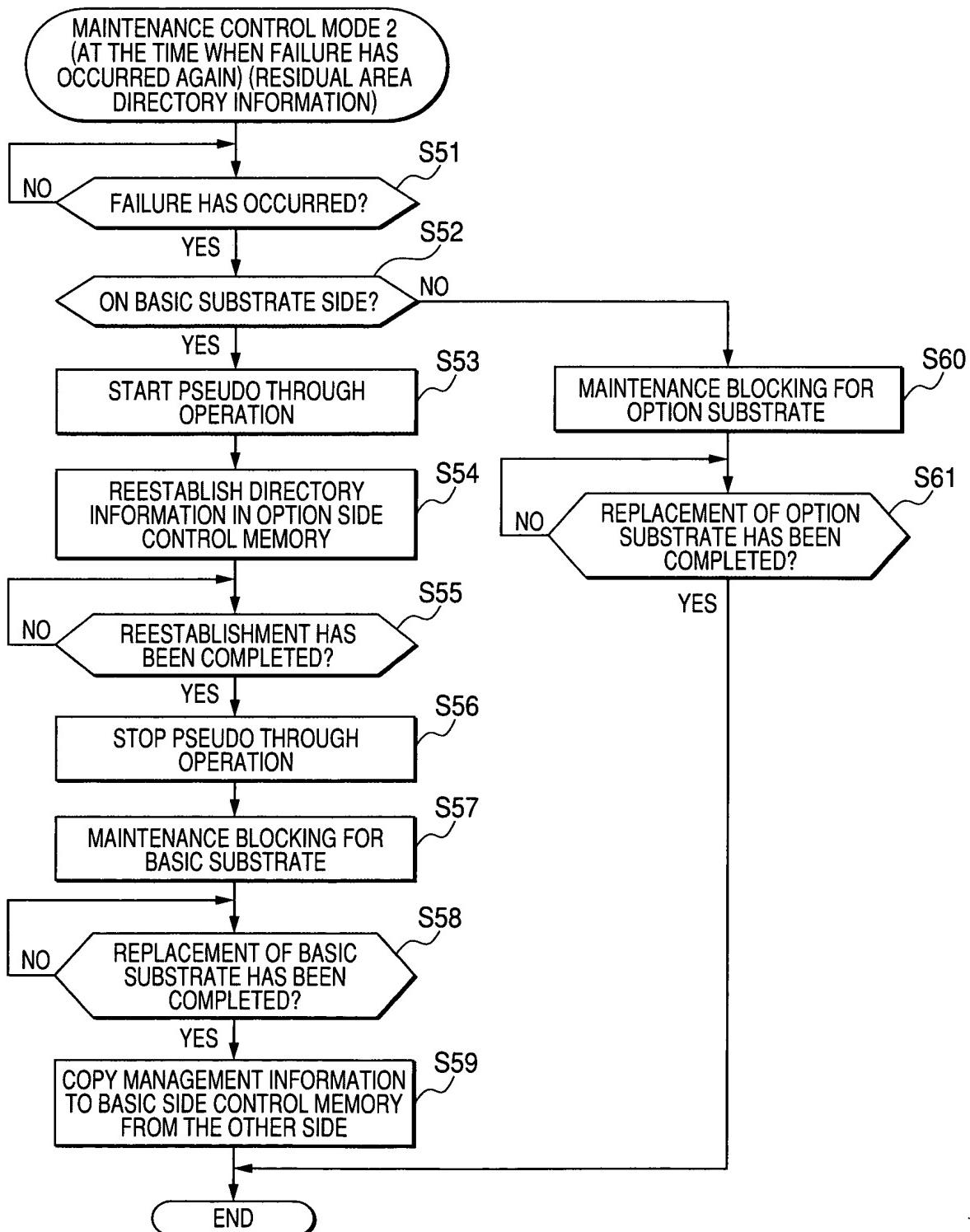
FIG. 13

FIG. 14A
FIG. 14B
FIG. 14C

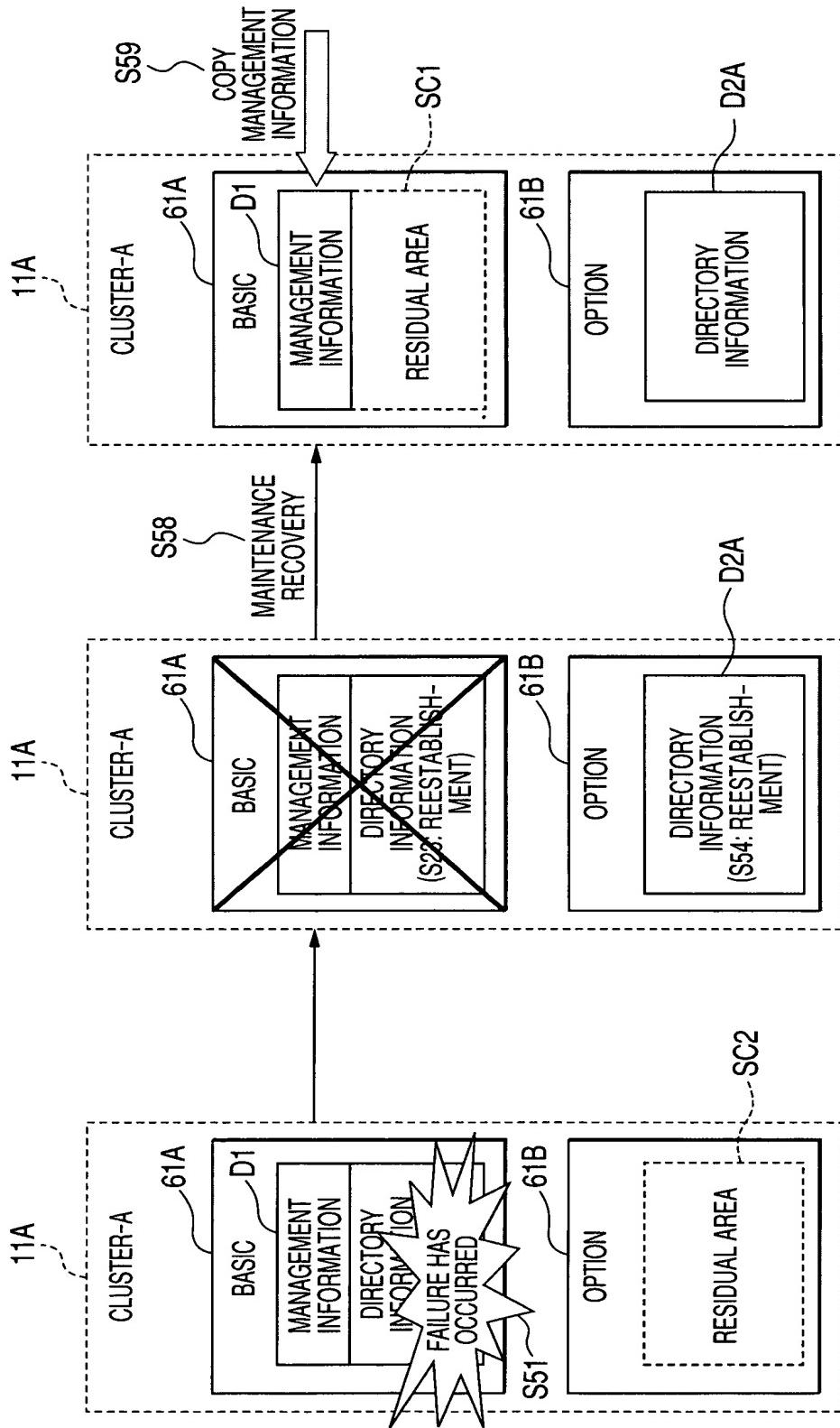
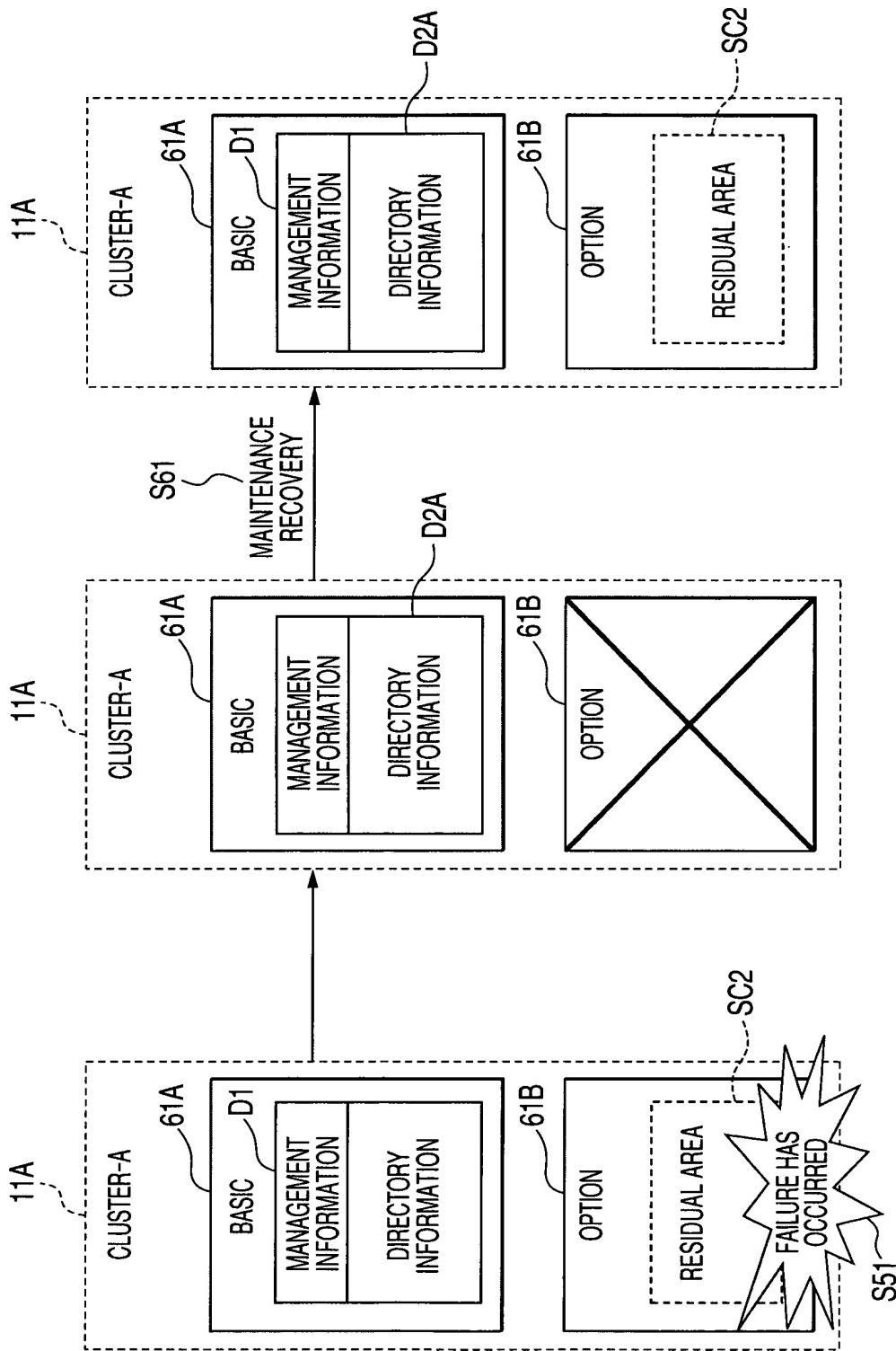


FIG. 15A

FIG. 15B

FIG. 15C



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FIG. 16

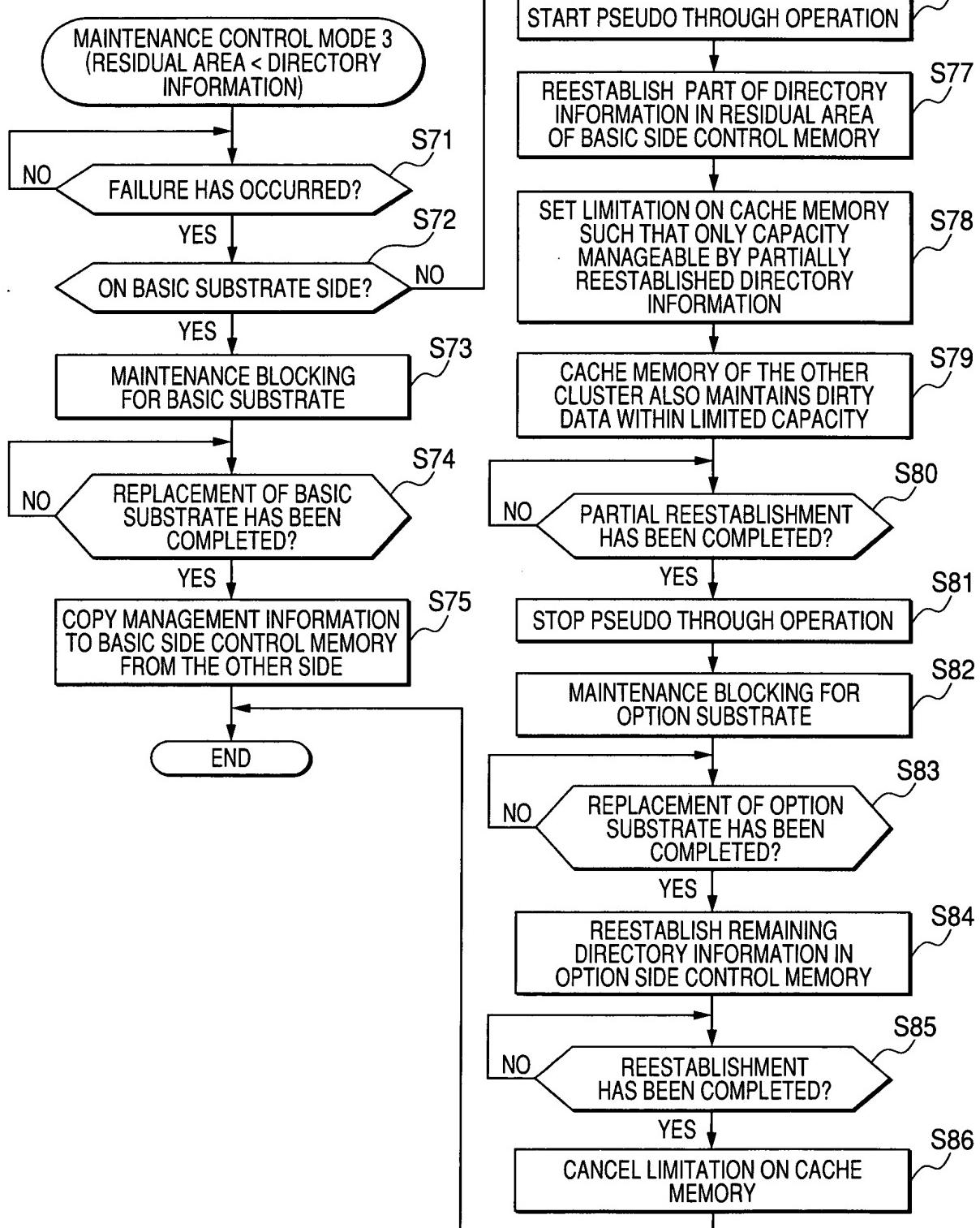


FIG. 17A

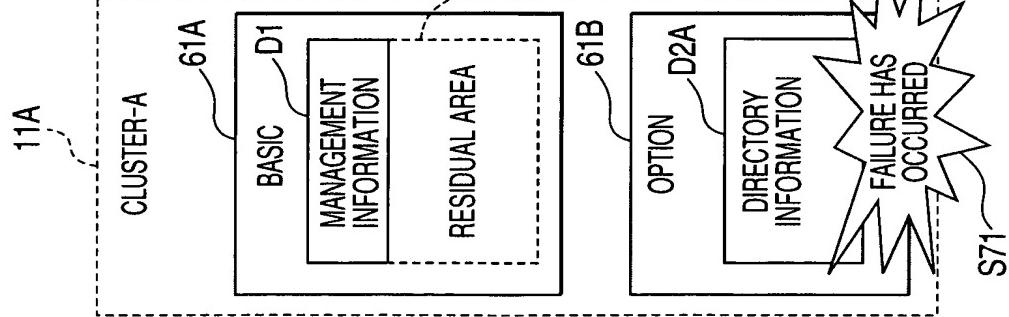


FIG. 17B

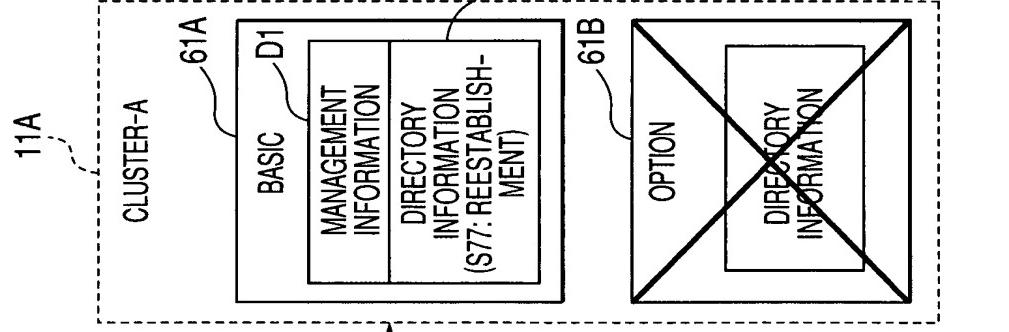
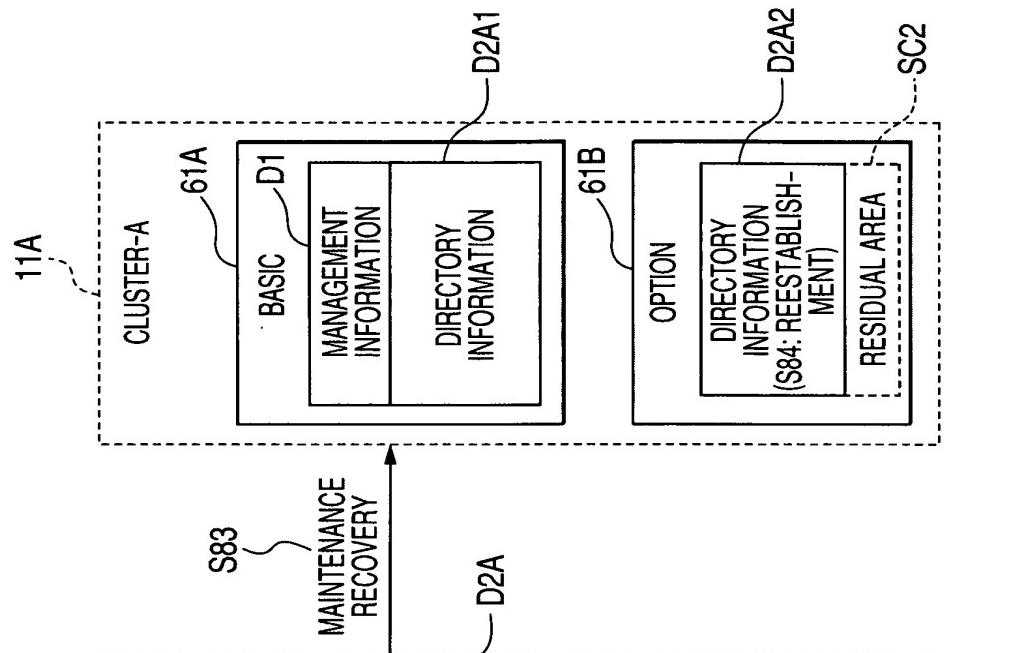


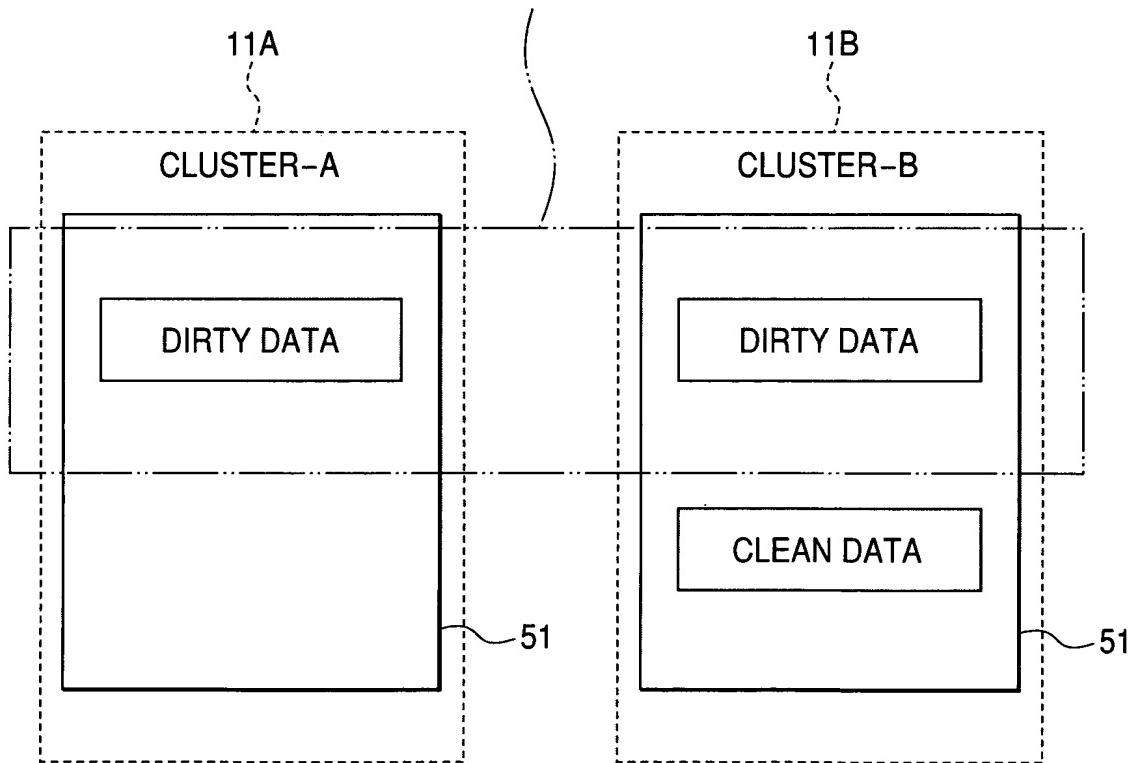
FIG. 17C



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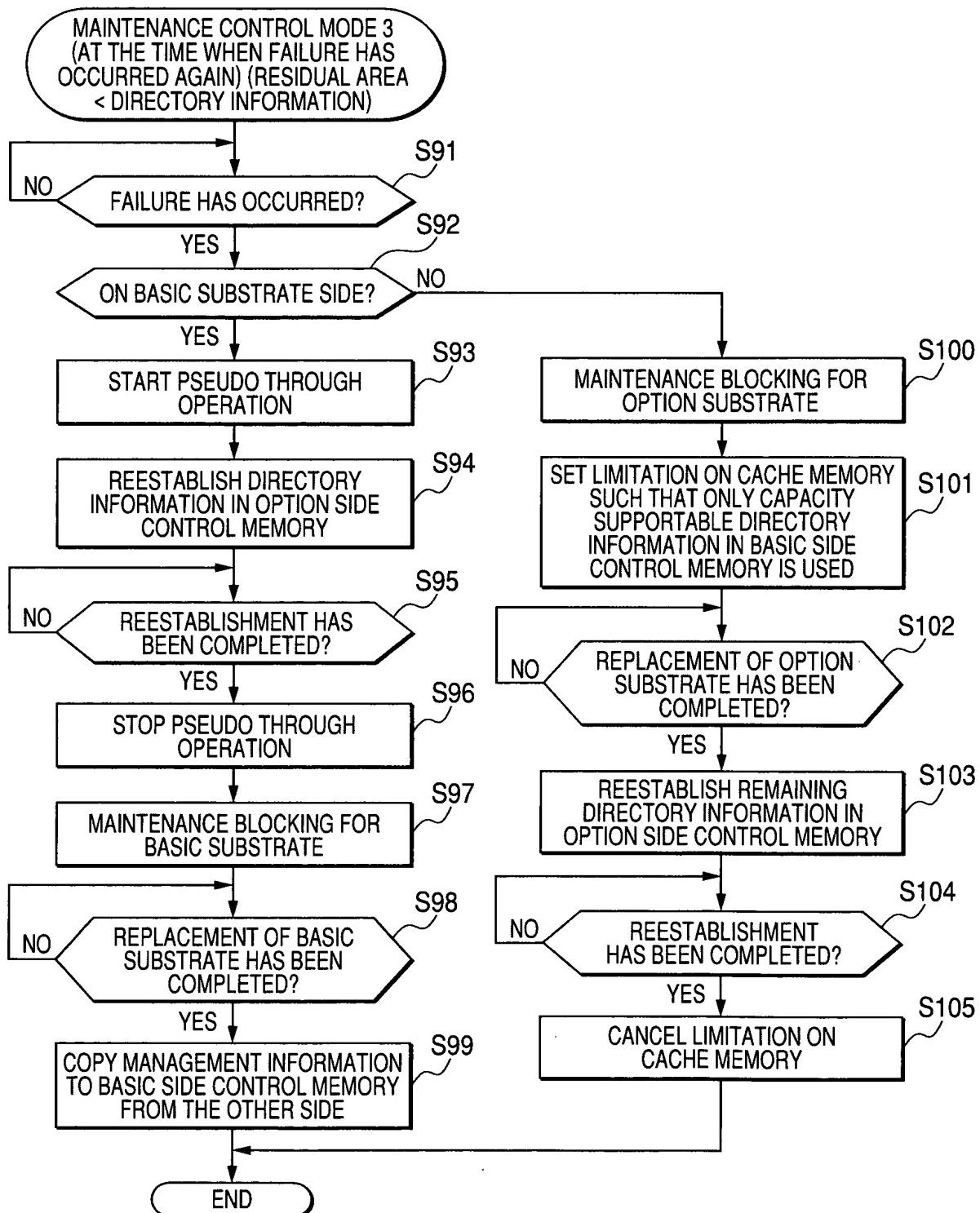
FIG. 18

CACHE CAPACITY SUPPORTABLE BY PARTIALLY REESTABLISHED DIRECTORY INFORMATION



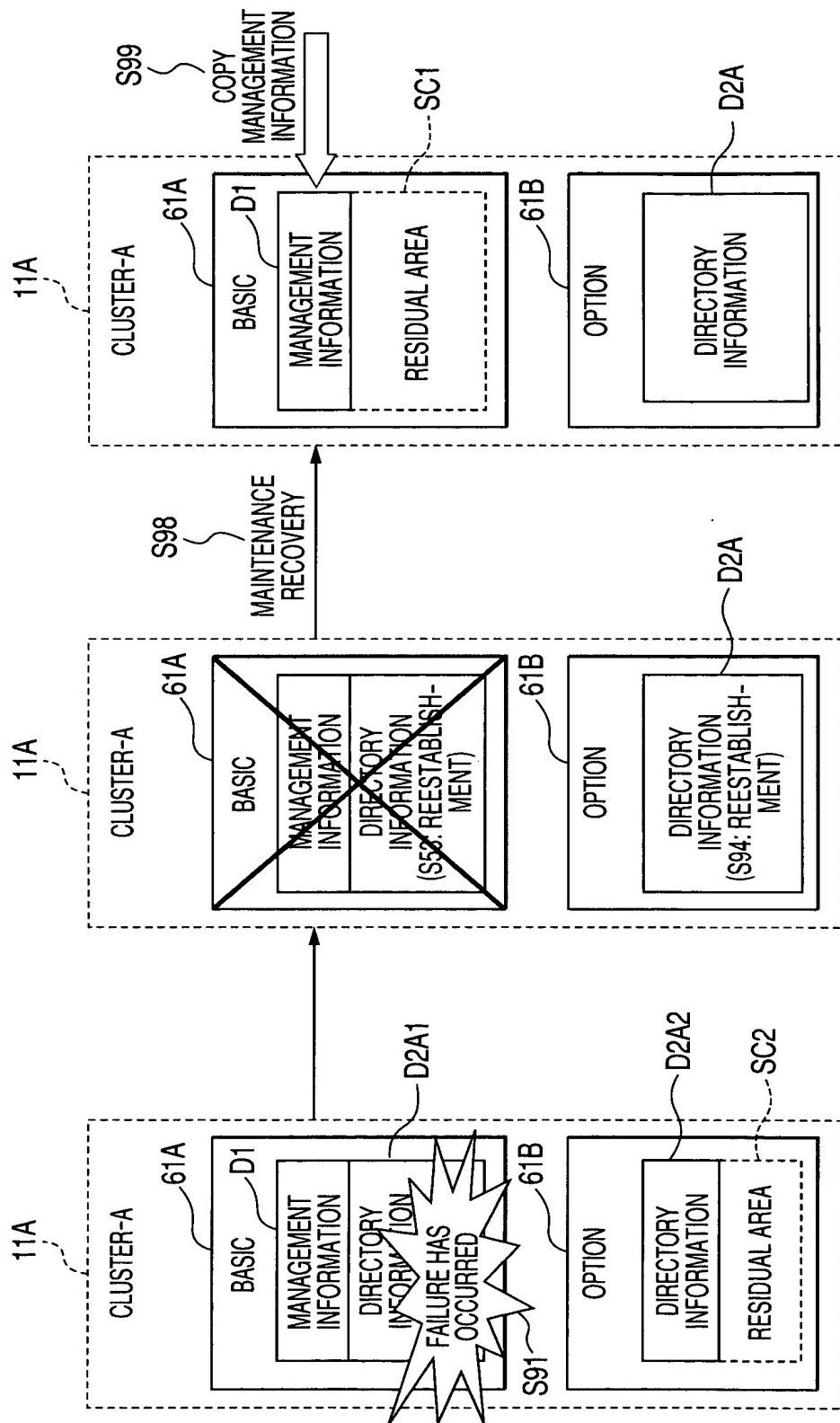
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FIG. 19



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FIG. 20A
FIG. 20B
FIG. 20C



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FIG. 21A FIG. 21B FIG. 21C

